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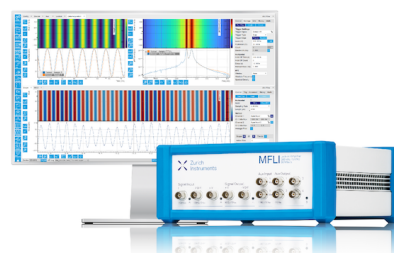
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P-Zn₃P₂ single nanowire metal-semiconductor field-effect transistors

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As far as we know, all the single nanowire (NW) metal-semiconductor field-effect transistors (MESFETs) reported are based on *n*-type NWs. We report MESFETs based on *p*-type Zn₃P₂ single NWs in this paper. The *p*-type Zn₃P₂ single NW MESFETs operate in the enhancement mode (*E*-mode). The source-drain current decreases with gate bias (V_G) increasing, confirming the *p*-type conductance of the Zn₃P₂ NWs. Typically, the *p*-type Zn₃P₂ single NW MESFET has an on/off current ratio of 10³, a threshold gate voltage of -0.4 V, and a maximum transconductance of 110 nS. © 2008 American Institute of Physics. [DOI: 10.1063/1.2960494]

I. INTRODUCTION

Semiconductor nanowires (NWs) are good building blocks for functional nanodevices, including the field-effect transistors (FETs), waveguides, photoconductive optical switches, sensors, light-emitting diodes, etc.^{1–10} Metal-insulator-semiconductor field-effect transistors (MISFETs) (Refs. 11–17) and metal-semiconductor field-effect transistors (MESFETs) based on single NWs have been reported.^{18–20} For wide applications of NW-FETs, both *n*- and *p*-channel are required. For example, complementary logic gate, which has a key characteristic of low static power dissipation and is especially superior in denser circuit integration, involves both *n*- and *p*-channel transistors. However, as far as we know, all the single NW MESFETs reported are based on *n*-type single NWs. In this paper, we report single NW MESFETs based on *p*-type Zn₃P₂ single NWs. The *p*-type Zn₃P₂ single NW MESFETs operate in the enhancement mode (*E*-mode). *E*-mode (normally off) FETs do not have a conductive channel at zero gate voltage, and have an advantage in high speed and low power consumption operation devices.¹⁹

II. EXPERIMENTS

The *p*-type Zn₃P₂ NWs were synthesized via the chemical-vapor deposition method in a tube furnace (the details were presented in Ref. 21). We used a mixture of Zn (99.99%) powders and InP (99.99%) fragments (with mass ratio of ~1:1) as the source and pieces of Si wafer covered with 10-nm-thick thermally evaporated Au catalysts as the substrates. The synthesis temperature was 850 °C. After the synthesis process, yellowish products were characterized using a field emission scanning electron microscope (FESEM) Amray 1910 FE and a high-resolution transmission electron microscope (HRTEM) Tecnai F30 equipped with an energy-dispersive x-ray (EDX) spectroscope. The electrical properties of the *p*-type Zn₃P₂ NWs were characterized by fabricat-

ing single NW MISFETs. The measurement results showed the average hole concentration and mobility at about 5.6 × 10¹⁶ cm⁻³ and 42.5 cm²/V·s, respectively.²¹

The MESFETs based on *p*-type Zn₃P₂ single NWs were fabricated as follows: First, the Zn₃P₂ NW suspension was dropped on oxidized Si substrates each with a SiO₂ layer of about 300 nm. Then, UV lithography, thermal evaporation, and lift-off processes were used to fabricate the source and

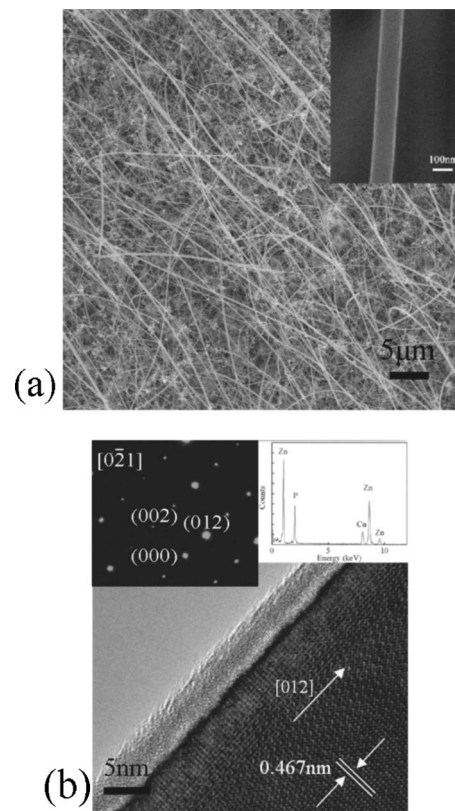


FIG. 1. (a) The FESEM image of as-synthesized Zn₃P₂ NWs. The inset is a magnified image that shows that the diameter of a typical Zn₃P₂ NW is about 100 nm. (b) The HRTEM image of a Zn₃P₂ NW. The inset at the upper-left corner is the corresponding SAED pattern. The inset at the upper-right corner is the EDX spectrum of the Zn₃P₂ NW.

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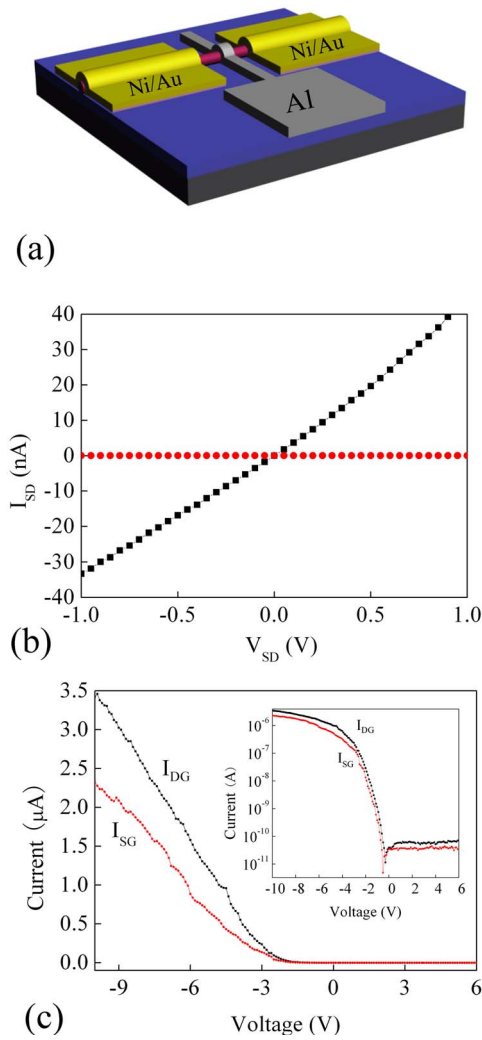


FIG. 2. (Color online) (a) The schematic illustration of the Zn₃P₂ single NW MESFET. (b) I_{SD} - V_{SD} curves before (the black line) and after (the red line) evaporating a top Al Schottky gate. (c) I - V curves between the source-gate and drain-gate, where the source or the drain was grounded. The inset is the I - V curves on an exponential scale.

drain Ohmic contact Ni/Au (10 nm/90 nm thick) electrodes. Lastly, a similar process was used to make a top surrounding Schottky Al gate electrode (100 nm thick) across the NW between the source and drain electrodes. The electrical transport measurements on the p -type Zn₃P₂ MESFETs were conducted using a semiconductor characterization system (Keithley 4200).

III. RESULTS AND DISCUSSIONS

Figure 1(a) shows a typical FESEM image of as-synthesized p -type Zn₃P₂ NWs. The inset is a magnified FESEM image. Each Zn₃P₂ NW has a smooth surface and a uniform diameter along the growth direction. The average diameter of the NWs is about 100 nm, and the length is several tens of microns.

The inset at the upper-right corner of Fig. 1(b) is the EDX spectrum taken from a Zn₃P₂ NW. It consists of only Zn and P signals with an atomic ratio of about 3:2. Figure 1(b) shows the HRTEM image of the Zn₃P₂ NW. The crystal planes with a spacing distance of about 0.467 nm can be seen

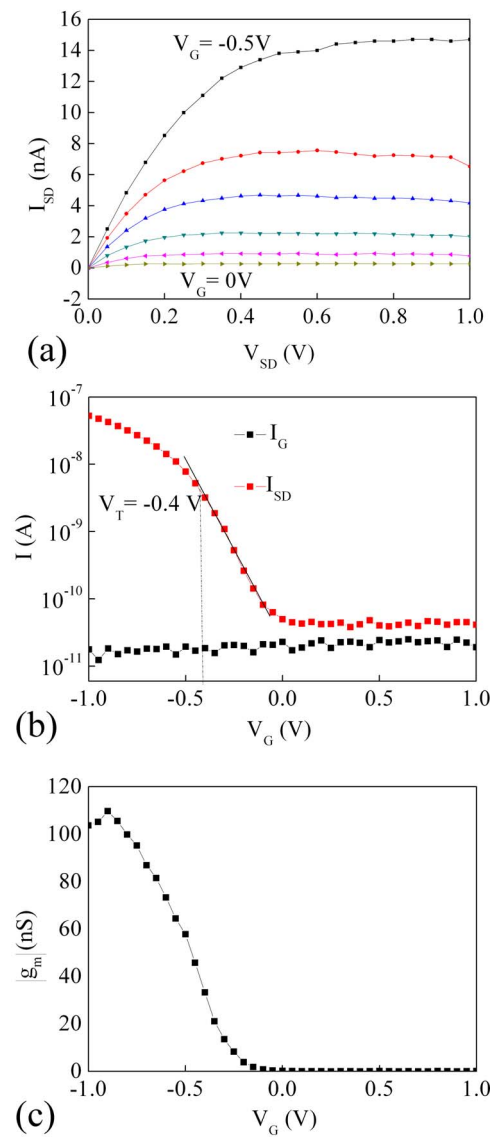


FIG. 3. (Color online) (a) I_{SD} - V_{SD} characteristics of a p -type Zn₃P₂ MESFET measured at room temperature under gate biases ranging from -0.5 to 0 V with a step of 0.1 V. (b) The I_{SD} - V_G (the red line) and I_G - V_G (the black line) curves of the MESFET measured at $V_{SD}=1$ V on an exponential scale. (c) The $|g_m|$ - V_G curve at $V_{SD}=1$ V.

along the growth direction. According to Ref. 22, these planes can be indexed as the tetragonal Zn₃P₂ (012) planes. The inset at the upper-left corner of Fig. 1(b) is the corresponding selected area electron diffraction (SAED) pattern recorded along the $[0\bar{2}1]$ zone axis. The HRTEM image, together with the SAED pattern, reveals that the Zn₃P₂ NW is a single crystal with the tetragonal structure, and its growth direction is $[012]$.

Figure 2(a) is a schematic illustration of the p -type Zn₃P₂ single NW MESFET. The gate length of the MESFET is about $3 \mu\text{m}$. The space distance between the source and drain electrodes is about $40 \mu\text{m}$.

Figure 2(b) shows the source-drain currents (I_{SD}) versus source-drain voltage (V_{SD}) before (the black line) and after (the red line) evaporating the top Al Schottky gate. Before evaporating the top Al Schottky gate, the $I_{SD} \sim V_{SD}$ relation is nearly a straight line, indicating the formation of quite

good Ohmic contacts between the Ni/Au electrodes and the p -type Zn_3P_2 NW. From the I - V curve and the dimensions of the NW channel, the resistivity of the p -type Zn_3P_2 NW is obtained to be about $9.3 \ \Omega \text{ cm}$. When a top Al Schottky gate was made, the p -type Zn_3P_2 NW channel was pinched off, and the I_{SD} was below several picoamperes.

Figure 2(c) shows I - V characteristics between the source-gate and drain-gate, where the source or the drain was grounded. The inset is the corresponding I - V characteristics on an exponential scale. We can see a good Schottky contact between the Al electrode and the p -type Zn_3P_2 NW. The on/off current ratio of the Schottky junction is larger than 10^4 when the gate bias changes from -10 to $+6 \text{ V}$.

Figure 3(a) shows the I_{SD} vs V_{SD} curves at various gate biases (V_{G}) for the p -type Zn_3P_2 NW MEFET. We can see that the NW MEFET is turned off at zero gate bias. This means that the as-fabricated NW MEFET is in the E -mode. Besides, for a given V_{SD} , I_{SD} decreases sharply when V_{G} increases from -0.5 to 0 V , indicating the p -type conduction characteristic of the NW.

Figure 3(b) shows the I_{SD} vs V_{G} (the red line) and gate leakage current (I_{G}) vs V_{G} (the black line) relations on an exponential scale measured at $V_{\text{SD}}=1 \text{ V}$. In our case, V_{G} is limited to be above -1 V to avoid excessive I_{G} leakage. From the I_{SD} - V_{G} curve, an on/off current ratio can be obtained to be about 10^3 when V_{G} changes from -1 to 1 V . The threshold gate voltage (V_{T}) obtained is about -0.4 V .

Figure 3(c) shows the absolute value of the transconductance ($g_m=dI_{\text{SD}}/dV_{\text{G}}$) vs the V_{G} curve. The maximum absolute value of transconductance is obtained to be about 110 nS at $V_{\text{G}}=-0.9 \text{ V}$.

IV. CONCLUSION

In conclusion, we report p -type Zn_3P_2 single NW MEFETs for the first time. A top surrounding Al Schottky contact gate was made. The p -type Zn_3P_2 single NW MEFET works in the E -mode. The on/off ratio is about 10^3 , the threshold voltage is about -0.4 V , and the maximum transconductance is about 110 nS .

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